

**PDPM Indian Institute of Information Technology,
Design & Manufacturing Jabalpur, Dumna Airport Road, Jabalpur - 482005**

Date: 24th June, 2019

Advertisement No.: IIITDM /SMDP-C2SD/2019/01

Candidates of Indian nationality are invited to appear for the Walk-in test/interview for project staff under Special Manpower Development Program - Chips to System Design (SMDP-C2SD) Deity-sponsored project. The position is purely temporary and renewable each year subject to satisfactory performance for a maximum of five years or till the completion of project whichever is earlier. Candidates selected for this position can also apply for the regular/part-time PhD Program in our institute provided satisfying the eligibility criteria for PhD Program. The details are given below:-

Title of the Project		
Special Manpower Development Program - Chips to System Design (SMDP-C2SD)		
Funding Agency	Ministry of Electronics & Information Technology (MeitY), Govt. of India	
Chief Investigator	Prof. P. N. Kondekar	
Dept./Centre	Electronics & Communication Engineering	
Post	Consolidated Pay	Minimum Qualification
Guest/Temporary Lecturer (1 Post)	Rs. 40,000/- pm	M.Tech/M.E in the field of Microelectronics/VLSI with knowledge in CAD tools (Cadence/Synopsys/Mentor Graphics/Xilinx) and chip designing.
Project- Assistant (1 Post)	Rs. 20,000/- pm	B.E/B. Tech in Electronics and Communication Candidates having good academic and research background will be given preference.
Date of Walk-in test/interview: 16/07/2019	Date of Walk-in test/interview: 16/07/2019	Venue: PDPM IIITD&M Jabalpur, Dumna Airport road, Jabalpur.

The candidates who fulfill the above qualifications/experience should appear for the written test/interview. Candidates must bring all original certificates, passport size photo and original Identity-Card along with one set of photo-copies for verification of educational qualifications, experience and identity of the candidate. No TA/DA will be paid for attending the test/interview.


Prof. P.N. Kondekar
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